



#3

PTO/SB/53 (02-01)

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REISSUE APPLICATION: CONSENT OF ASSIGNEE; STATEMENT OF NON-ASSIGNMENT		Docket Number (Optional) 501.34214R00
This is part of the application for a reissue patent based on the original patent identified below.		
Name of Patentee(s) Tsugio TAKAHASHI et al.		
Patent Number	5,966,341	Date Patent Issued October 12, 1999
Title of Invention SEMICONDUCTOR MEMORY		
1. <input checked="" type="checkbox"/> Filed herein is a statement under 37 CFR 3.73(b). (Form PTO/SB/96)		
2. <input type="checkbox"/> Ownership of the patent is in the inventor(s), and no assignment of the patent is in effect.		
One of boxes 1 or 2 above must be checked. If multiple assignees, complete this form for each assignee. If box 2 is checked, skip the next entry and go directly to "Name of Assignee". The written consent of all assignees and inventors owning an undivided interest in the original patent is included in this application for reissue.		
The assignee(s) owning an undivided interest in said original patent is/are <u>See Addendum</u> and the assignee(s) consents to the accompanying application for reissue.		
Name of assignee/inventor (if not assigned)		
Signature	Date	
Typed or printed name and title of person signing for assignee (if assigned) See Addendum		

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Addendum

Invention Title:

SEMICONDUCTOR MEMORY

Name of Assignee:

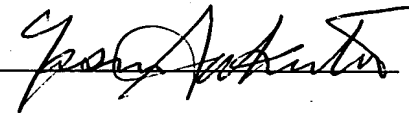
HITACHI, LTD. & HITACHI DEVICE ENGINEERING CO., LTD.

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee:

28 / February / 2002
Date

Yasuo SAKUTA

Signature



Executive Managing Director,
Intellectual Property Group
Hitachi, Ltd.

06 / March / 2002
Date

Kuniharu OSAKABE

Signature



Title President
Hitachi Device Engineering Co., Ltd.



PTO/SB/96 (08-00)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Tsugio TAKAHASHI et al.Application No./Patent No.: 5,966,341 Filed/Issue Date: October 12, 1999Entitled: SEMICONDUCTOR MEMORYSee Addendum, a Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
The extent (by, percentage) of its ownership interest is _____ %

in the patent application/patent identified above by virtue of either;

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 8129, Frame 0386, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

2. From: _____ To: _____

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3. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

- ☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.
See Addendum

Date_____
Typed or printed name_____
Signature_____
Title

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Addendum

Invention Title:

SEMICONDUCTOR MEMORY

Name of Assignee:

HITACHI, LTD. & HITACHI DEVICE ENGINEERING CO., LTD.

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee:

28/February/2002
Date

Yasuo SAKUTA

Signature

Executive Managing Director,
Intellectual Property Group
Hitachi, Ltd.

06/March/2002
Date

Kuniharu OSAKABE

Signature

Title President
Hitachi Device Engineering Co., Ltd.

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OIP
REISSUE APPLICATION DECLARATION BY THE INVENTOR

Docket Number (Optional)

501.34214R00

MAR 27 2002
U.S. PATENT & TRADEMARK OFFICE

As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 5,966,341, granted October 12, 1999, and for which a reissue patent is sought on the invention entitled SEMICONDUCTOR MEMORY

the specification of which

☐ is attached hereto.

☒ was filed on October 12, 2001 as reissue application number 09 / 974,962
and was amended on _____
(If applicable)

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

☐ by reason of a defective specification or drawing.

☒ by reason of the patentee claiming more or less than he had the right to claim in the patent.

☐ by reason of other errors.

At least one error upon which reissue is based is described below. If the reissue is a broadening reissue, such must be stated with an explanation as to the nature of the broadening:

The original patent is believed to be wholly or partially inoperative or invalid for failure to include new claims 10-19. These claims correspond to the original claims 1-9, but have been broadened to delete the recitation of the phrase "pairs of" referred to in the original claims, for example, with regard to "a plurality of pairs of data lines."

[Page 1 of 2]

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(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 2)

Docket Number (Optional)
501.34214R00

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant. As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith.

Name(s)	Registration Number
Gregory E. Montone	28,141
Larry N. Anagnos	32,392
Donald R. Antonelli	20,296

See Attachment

Correspondence Address: Direct all communications about the application to:

☒ Customer Number

Type Customer Number here

Place Customer Number Bar
Code Label here☐ Firm or
Individual Name

Address

Address

City

State

Zip

Country

Telephone

Fax

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

Full name of sole or first inventor (given name, family name)

Tsugio TAKAHASHI

Inventor's signature *Tsugio Takahashi*Date *28/February/2002*Residence
Tokyo, JapanCitizenship
Japan

Mailing Address 3-5-3-202, Motohongo-cho, Hachioji-shi, Tokyo, Japan

Full name of second joint inventor (given name, family name)

Goro KITSUKAWA

Inventor's signature *Goro Kitsukawa*Date *12/March/2002*Residence
Tokyo, JapanCitizenship
Japan

Mailing Address 1-8-32-104, Takasago, Kisarazu-shi, Chiba, Japan

Full name of third joint inventor (given name, family name)

Takesada AKIBA

Inventor's signature *Takesada Akiba*Date *5/March/2002*Residence
Tokyo, JapanCitizenship
Japan

Mailing Address 2-14-14, Shiroyamate, Hachioji-shi, Tokyo, Japan

☒ Additional joint inventors are named on separately numbered sheets attached hereto.

Please type a plus sign (+) inside this box → ☐

PTO/SB/02A (11-00)

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MAR 27 2002

DECLARATION

ADDITIONAL INVENTOR(S)

Supplemental Sheet

Page 1 of 1

Name of Additional Joint Inventor, if any:		<input type="checkbox"/> A petition has been filed for this unsigned inventor	
Given Name (first and middle [if any])		Family Name or Surname	
Yasushi		KAWASE	
Inventor's Signature <i>Yasushi Kawase</i>		Date <i>5/March/2002</i>	
Residence: City Tokyo	State	Country Japan	Citizenship Japan
Mailing Address 2-5-35, Ichiban-cho, Tachikawa-shi, Tokyo, Japan			
Mailing Address			
City Tokyo	State	ZIP	Country Japan
Name of Additional Joint Inventor, if any:		<input type="checkbox"/> A petition has been filed for this unsigned inventor	
Given Name (first and middle [if any])		Family Name or Surname	
Masayuki		NAKAMURA	
Inventor's Signature <i>Masayuki Nakamura</i>		Date <i>28/2/2002</i>	
Residence: City Tokyo	State	Country Japan	Citizenship Japan
Mailing Address 5-29-63, Kabe-cho, Ome-shi, Tokyo, Japan			
Mailing Address			
City Tokyo	State	ZIP	Country Japan
Name of Additional Joint Inventor, if any:		<input type="checkbox"/> A petition has been filed for this unsigned inventor	
Given Name (first and middle [if any])		Family Name or Surname	
Inventor's Signature		Date	
Residence: City	State	Country	Citizenship
Mailing Address			
Mailing Address			
City	State	ZIP	Country

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Patent Number: 5,966,341
Issue Date: October 12, 1999
Invention Title: SEMICONDUCTOR MEMORY
Attorney Docket Number: 501.34214R00

Reissue Application Declaration by the Inventor (PTO/SB/51)
Registered Practitioner Information-Supplemental Sheet

Name	Registration Number	Name	Registration Number
William I. Solomon	28,565		
Gregory E. Montone	28,141		
Ronald J. Shore	28,577		
Donald E. Stout	26,422		
Alan E. Schiavelli	32,087		
James N. Dresser	22,973		
Carl I. Brundidge	29,621		
Paul J. Skwierawski	32,173		